Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8513	((dram (dynamic near2 random near4 (memory or storage or array or device)) or sdram or sram) with (bank or block or cell or sector or page)) same (subset or portion)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L2	1062	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L3	28879	((first or primary or upper or lower or "A" or "B") near4 (port or interfac\$3 or device or logic or circuit)) same ((first or upper or one) with (subset or part or portion) with (cell or sector or page or bank or block))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:11
L4	31675	((first or primary or second\$4 or upper or lower or "A" or "B") near4 (port or interfac\$3 or device or logic or circuit)) same ((first or upper or one) with (subset or part or portion) with (cell or sector or page or bank or block))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:09
L5	5170	(arbiter or switch or (control\$4 near3 (logic or device or circuit or process\$3))) same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L6	5893	(arbiter or switch or (control\$4 near3 (logic or device or circuit or process\$3))) same 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L7	1319	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:13

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L8	441	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) same 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:13
L9	55	((((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) same 6).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:30
L10	115	(integrat\$3 near5 (circuit or logic or device or circuitry)) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L11	56	(@pd<"20010612") and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:38
L12	5343	((plurality or group or dual) with (port or interfac\$3 or logic or device or circuit)) same ((prevent\$3 or allow\$4 or forc\$3 or lock\$3 or block\$3) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3)) same ((bank or cell or sector or page or block) with (subset or portion or part))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:32
L13	2878	(arbiter or switch or logic or circuit) same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:47
L14	835	(integrat\$3 near5 (circuit or logic or device or circuitry)) and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L15	74	(((dram (dynamic near2 random near4 (memory or storage or array or device)) or sdram or sram) with (bank or block or cell or sector or page)) same (subset or portion)) and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:36

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L16	44	((concurrent\$3 or parallel\$3 or simultan\$4 or ("SAME" adj time)) with (access\$3 or request\$3 or load\$3 or writ\$3 or stor\$3 or read\$3 or updat\$3 or modif\$3)) and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:37
L17	12	(@pd<"20010612") and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:46
L18	26	(@pd<"20010612") and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:51
L19	3	arbiter same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:50
L20	70	arbiter and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L21	64	arbit\$4 same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:50
L22	39	(@pd<"20010612") and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L23	85	arbit\$3 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L24	29	(@pd<"20010612") and 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:57
L25	5638	(arbiter or (arbit\$3 near4 (logic or circuit or device))) same ((select\$3 or switch\$3 or control\$3) with (access\$3 or read\$3 or load\$3 or transfer\$4 or writ\$3 or stor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:16

L26	13371	((multiple or multi\$4 or dual or plurality or two) with (interfac\$3 or port or device or logic or circuit)) same ((lock\$3 or block\$3 or prevent\$3 or allow\$4) with ((subset or portion or part) with (cell or sector or bank or page or area or region or zone)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:18
L27	22172	((multiple or multi\$4 or dual or plurality or two or first or primary or second\$4) with (interfac\$3 or port or device or logic or circuit)) same ((lock\$3 or block\$3 or prevent\$3 or allow\$4) with ((subset or portion or part) with (cell or sector or bank or page or area or region or zone)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:18
L28	1	25 same 27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:21
L29	26	25 and 27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:28
L30	25	((memory storage or array) with (bank or sector or cell or subset or sector or page)) and 29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:21
L31	0	(25 and 27).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 17:28

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	70974	((multiple or plurality or group or number or multi\$4 or dual) with (interfac\$3 or port or device or control\$4 or logic or circuit)) same ((prevent\$3 or lock\$3 or limit\$4 or block\$3) with (access\$3 or read\$3 or writ\$3 or stor\$3 or load)) same (bank or sector or cell or page or subset or region or zone or block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 20:54
L2	47	((arbiter or (arbit\$4 near4 (logic or device or circuit))) same ((switch\$3 or select\$3 or elect\$4) with (port or interfac\$3 or device)) same (bank or sector or cell or page or block or region or subset)) same 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 20:59
L3	29	(@pn<"20010612") and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 21:12

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8513	((dram (dynamic near2 random near4 (memory or storage or array or device)) or sdram or sram) with (bank or block or cell or sector or page)) same (subset or portion)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L2	1062	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L3	28879	((first or primary or upper or lower or "A" or "B") near4 (port or interfac\$3 or device or logic or circuit)) same ((first or upper or one) with (subset or part or portion) with (cell or sector or page or bank or block))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:11
L4	31675	((first or primary or second\$4 or upper or lower or "A" or "B") near4 (port or interfac\$3 or device or logic or circuit)) same ((first or upper or one) with (subset or part or portion) with (cell or sector or page or bank or block))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:09
L5	5170	(arbiter or switch or (control\$4 near3 (logic or device or circuit or process\$3))) same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L6	5893	(arbiter or switch or (control\$4 near3 (logic or device or circuit or process\$3))) same 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:12
L7	1319	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:13

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L8	441	(((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) same 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:13
L9	55	((((dual or multiple or plurality or more or mulit\$4) with (port or interfac\$3 or logic or circuit or device)) same ((lock\$3 or block\$3 or limit\$4 or prevent\$3 or forc\$3 or permit\$4 or allow\$4) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3 or modif\$4))) same 6).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:30
L10	115	(integrat\$3 near5 (circuit or logic or device or circuitry)) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L11	56	(@pd<"20010612") and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:38
L12	5343	((plurality or group or dual) with (port or interfac\$3 or logic or device or circuit)) same ((prevent\$3 or allow\$4 or forc\$3 or lock\$3 or block\$3) with (access\$3 or read\$3 or load\$3 or writ\$3 or stor\$3 or updat\$3)) same ((bank or cell or sector or page or block) with (subset or portion or part))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:32
L13	2878	(arbiter or switch or logic or circuit) same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:47
L14	835	(integrat\$3 near5 (circuit or logic or device or circuitry)) and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:35
L15	74	(((dram (dynamic near2 random near4 (memory or storage or array or device)) or sdram or sram) with (bank or block or cell or sector or page)) same (subset or portion)) and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:36

L16	44	((concurrent\$3 or parallel\$3 or simultan\$4 or ("SAME" adj time)) with (access\$3 or request\$3 or load\$3 or writ\$3 or stor\$3 or read\$3 or updat\$3 or modif\$3)) and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:37
L17	. 12	(@pd<"20010612") and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:46
L18	26	(@pd<"20010612") and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:51
L19	3	arbiter same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:50
L20	70	arbiter and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L21	64	arbit\$4 same 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:50
L22	39	(@pd<"20010612") and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L23	85	arbit\$3 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:53
L24	29	(@pd<"20010612") and 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/02/20 16:57